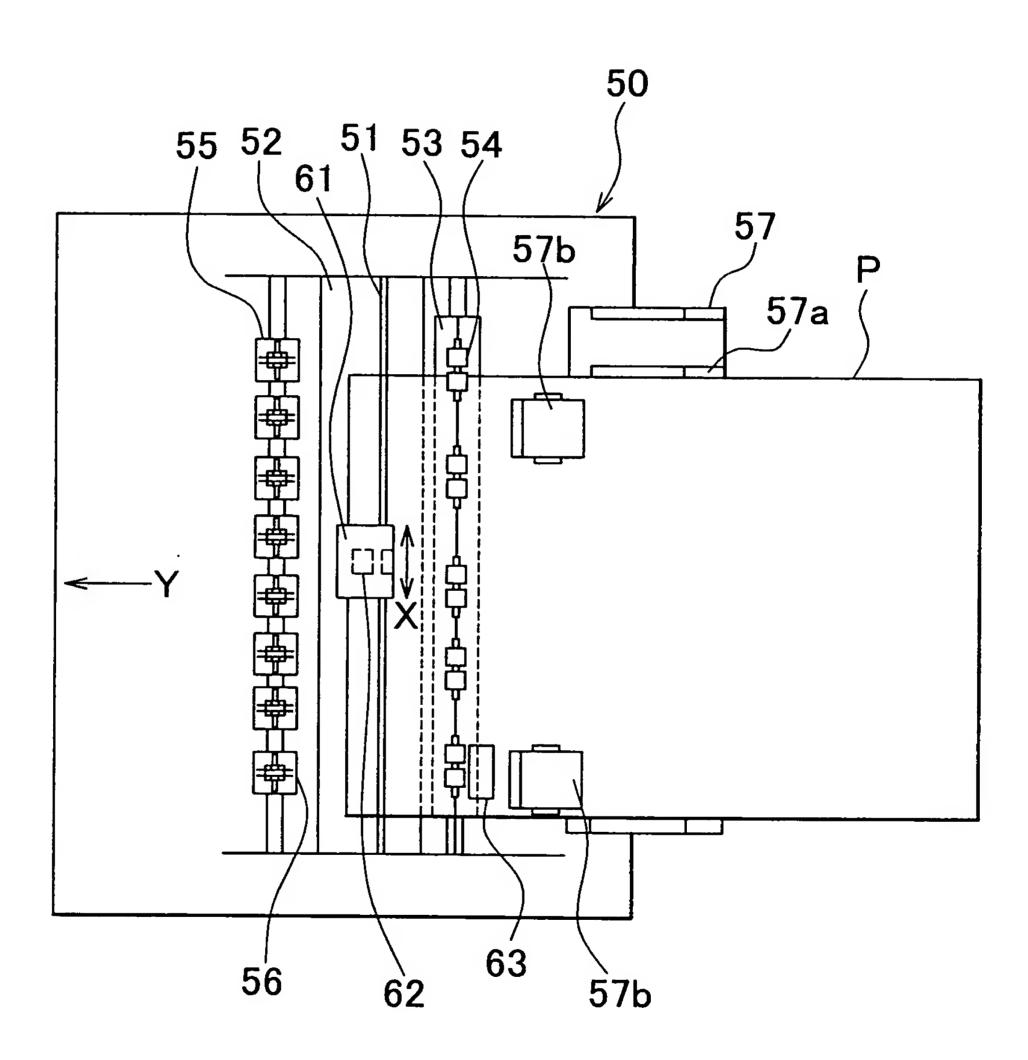
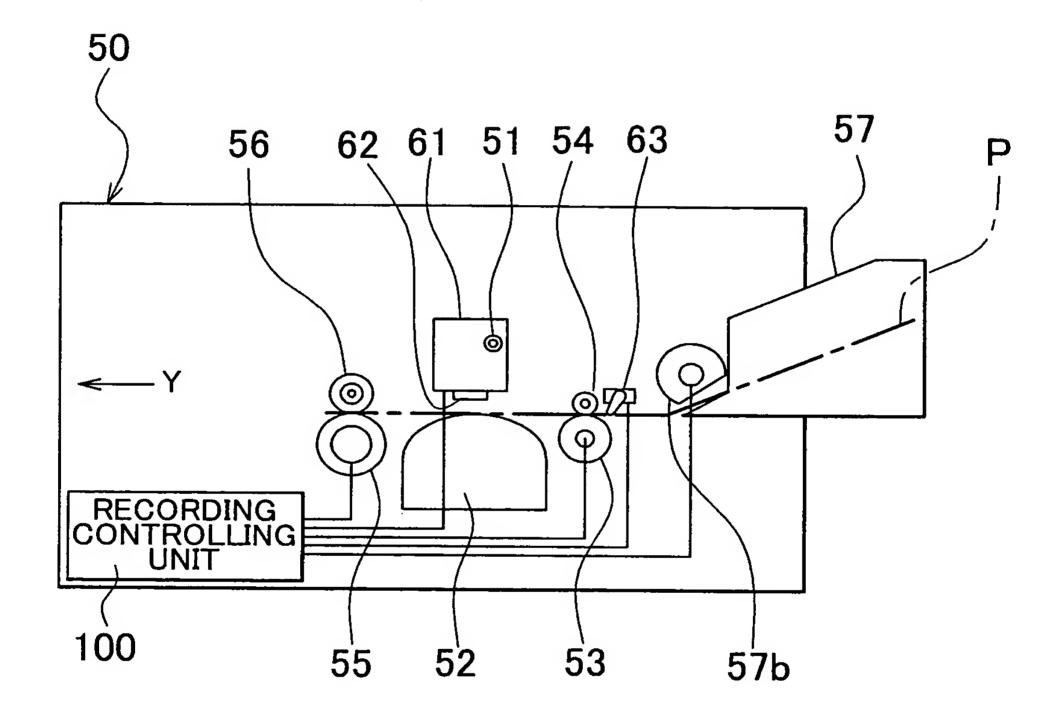


F I G. 1



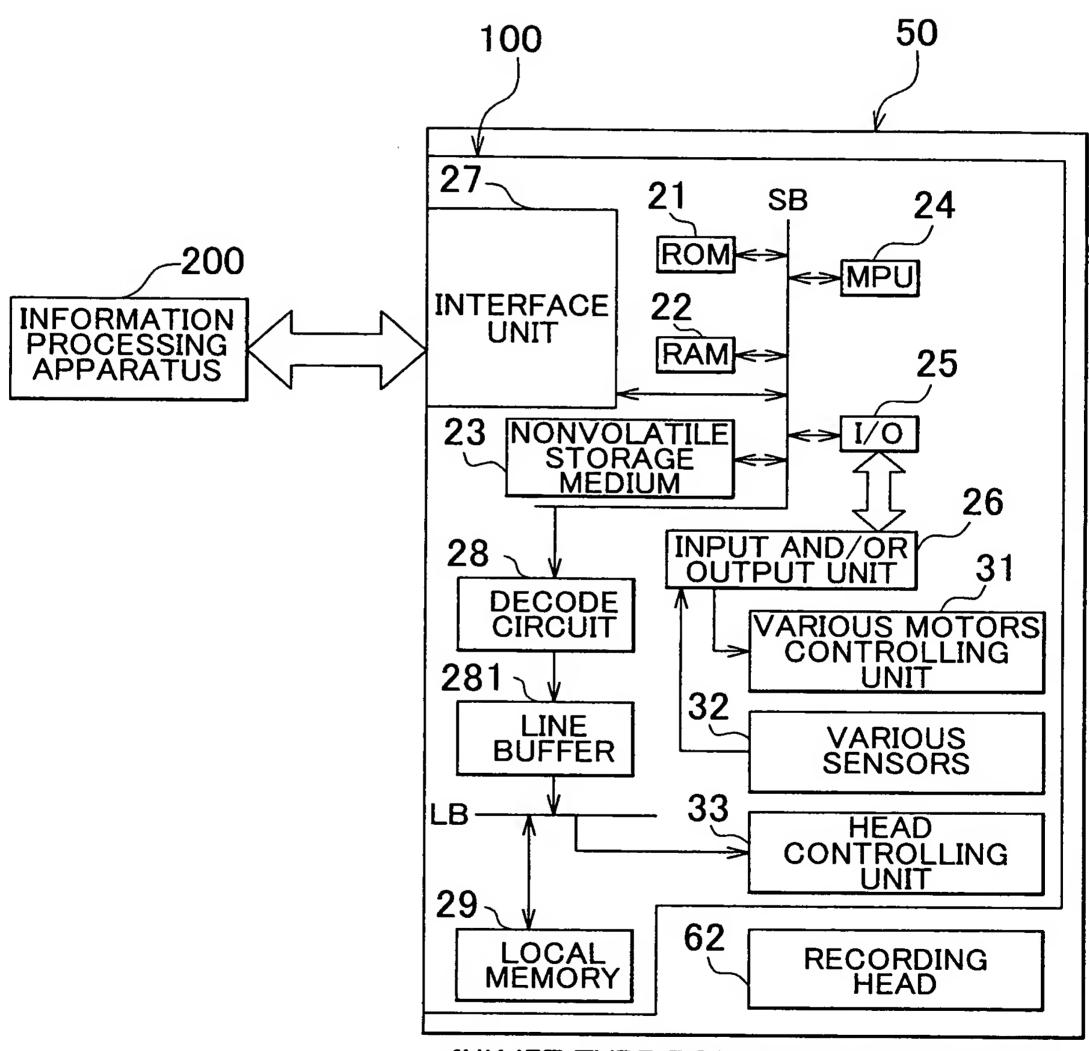


F I G. 2



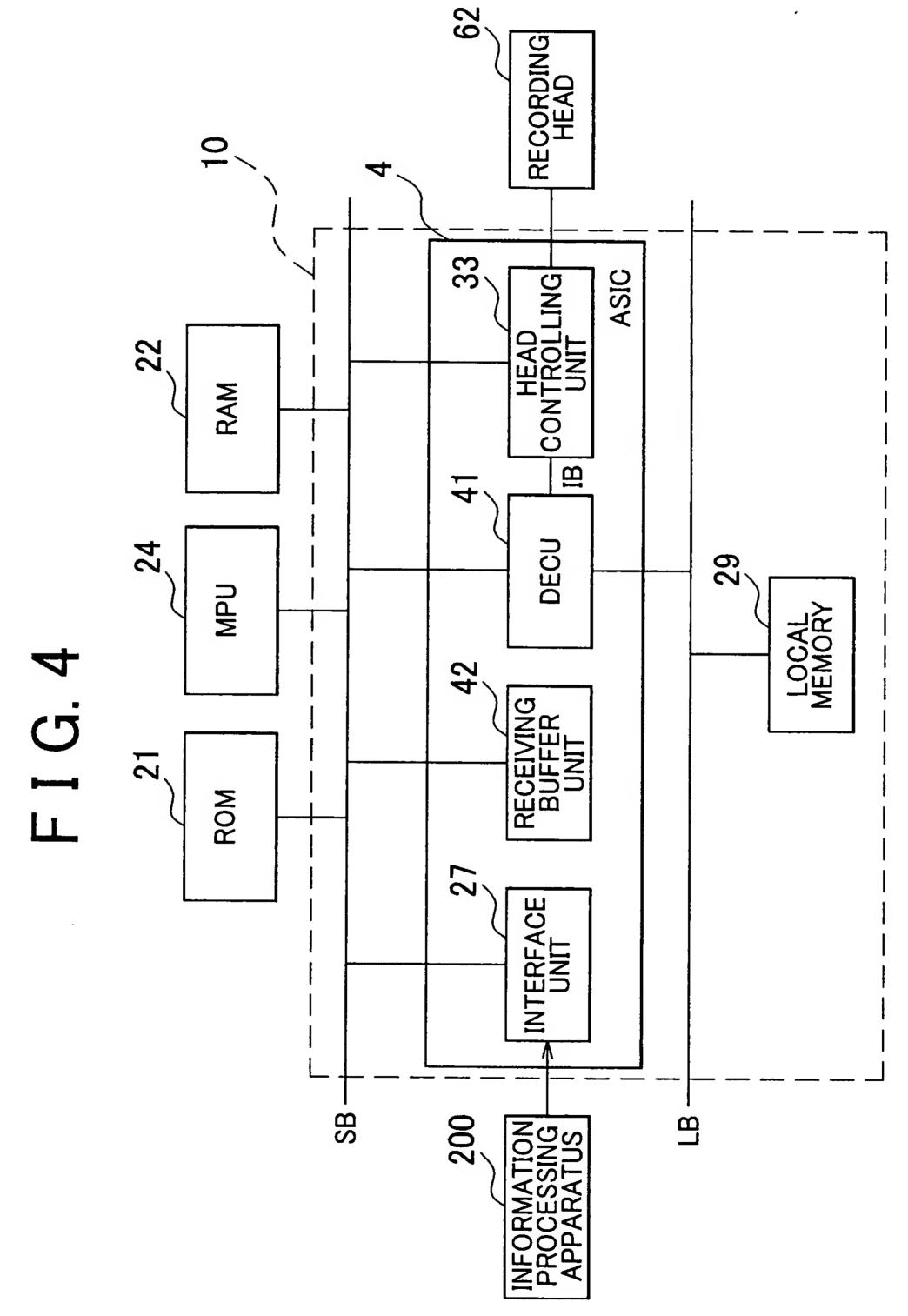


F I G. 3



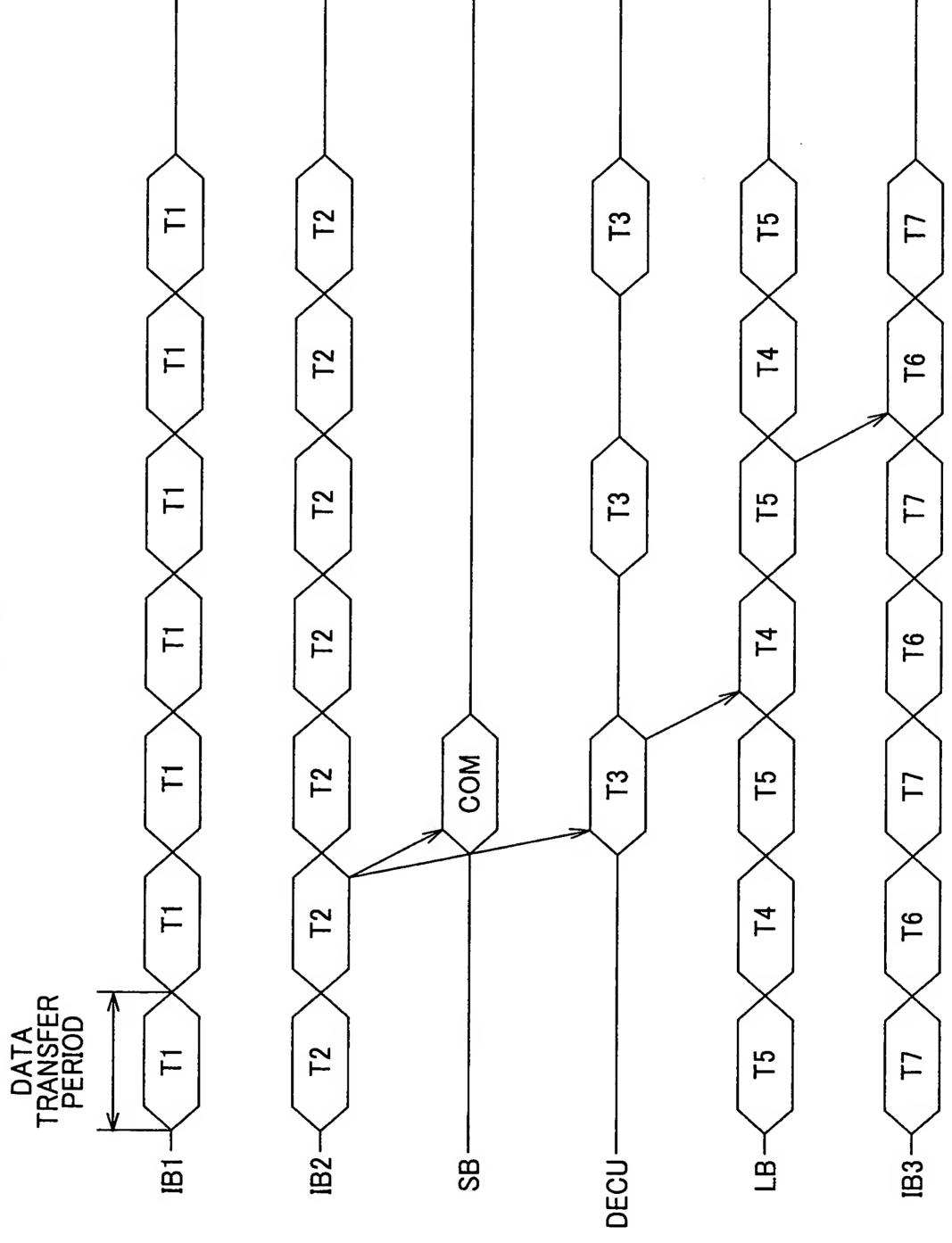
INKJET TYPE RECORDING APPARATUS







F I G. 5

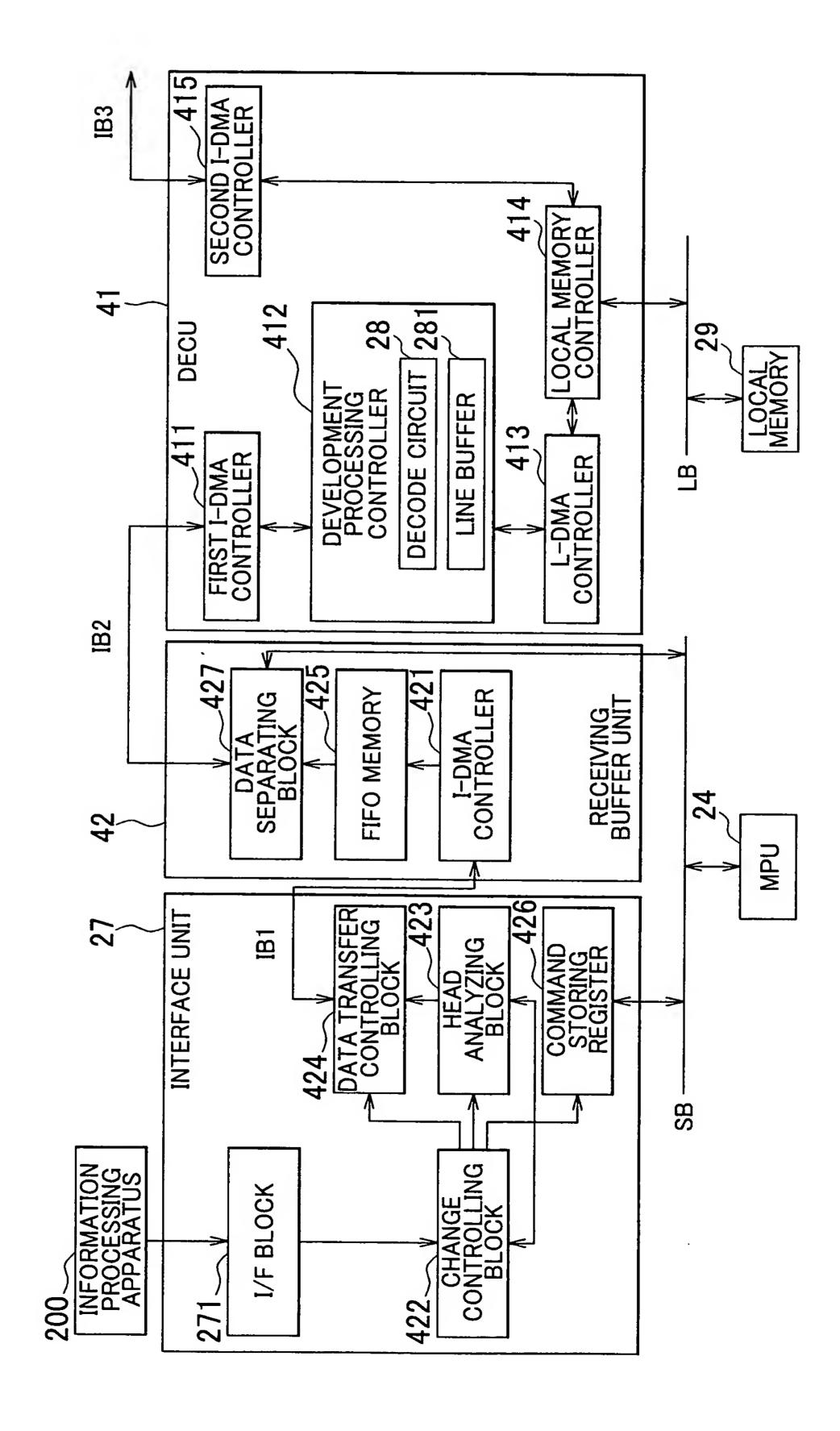




IB3 SECOND I-CONTROL LOCAL MEMORY CONTROLLER 41 281 28 DECU 29 DECODE CIRCUIT DEVELOPMENT PROCESSING CONTROLLER **LINE BUFFER** 413 FIRST I-DMA CONTROLLER LER **P** L-DMA CONTROLL **IB2** 425 424 SFER ING MEMORY CZING DATA ARAT 3LOCK ANAL BI SEP/B DATA CONT BI FIFO 24 RECEIVING BUFFER UNIT MPU 42 I-DMA CONTROLLER COMMAND STORING REGISTER CHANGE CONTROLL BLOCK 181 421 SB

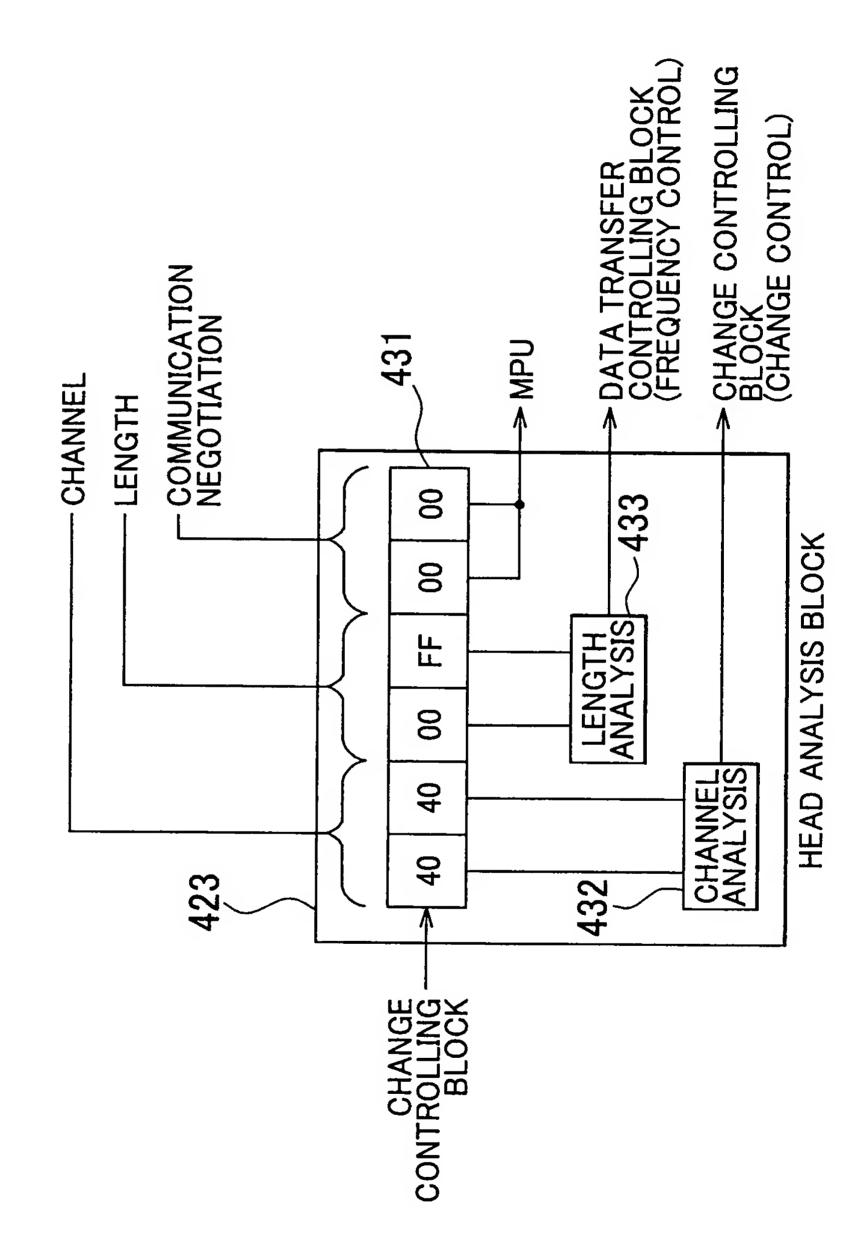


F I G. 7





F I G. 8





F I G. 9

OPERATION CONDITION
MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS
LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS
NUMBER OF BYTES IN 1 LINE: 16 BYTES

DECU TRANSFER ST FE 01 MAIN MEMORY FACE A 01 01 01 FE 01 **FACE B** 03 02 TRANSFER S2 03 02 78 55 FACE A 01 01 01 02 44 FB **FACE B** FF FE TRANSFER S3 78 55 FACE A 01 01 01 02 78 55 11 06 66 12 **FACE B** 77 45 TRANSFER S4 44 FB 89 10 FACE A 01 01 01 02 78 55 44 55 FB **FACE B** 10 FA TRANSFER S5 FF FE 20 08 FACE A 01 01 01 02 78 55 44 FF FF FF FF FF 12 13 **FACE B** 14 15 TRANSFER S6 11 06 TRANSFER D1 16 17 FACE A 01 01 01 02 78 55 44 FF FF FF FF FF 11 11 11 18 19 **FACE B** 20 FD TRANSFER \$7 66 12 11 02 FACE A 98 B0 FACE B 66 12 F2 FC TRANSFER S8 77 45 AB 03 FACE A FFFE FACE B 66 12 77 45 FC FD TRANSFER S9 89 10 **FACE A** FACE B 66 12 77 45 89 10 TRANSFER S10 55 FB FACE A FACE B 66 12 77 45 89 10 55 TRANSFER S11 10 FA FACE A FACE B 66 12 77 45 89 10 55 10 10 10 10 10 10 TRANSFER S12 20 08 **TRANSFER D2** FACE A FACE B 66 12 77 45 89 10 55 10 10 10 10 10 10 20 20 20



F I G. 10

DECU

TDANSEED 012 12 13	1									
TRANSFER S13 12 13	20.00	T 00 00	140 40	1	,	,	,			
FACE A		20 20	12 13		ļ		ļ			
FACE B	 		<u> </u>	<u></u>	<u> </u>		<u> </u>	<u> </u>	L	ł
TRANSFER S14 14 15	00.00	100.00	14040	1 2 2 2 2	,	<u></u>				_
FACE A	20 20	20 20	12 13	14 15						
FACE B	<u> </u>	L	<u></u>					<u> </u>		
TRANSFER S15 16 17	00.00	T60.00	14646	1 4 3 4 5	γ	,				•
FACE A	20 20	20 20	12 13	14 15	16 17					
FACE B		<u> </u>	l	<u> </u>			<u> </u>			
TRANSFER \$16 18 19		100.00	1 4 5 4 5		·					
FACE A	20 20	20 20	12 13	14 15	16 17	18 19				, i
FACE B			L	<u> </u>						
TRANSFER S17 20 FD	00.00	1-0-00-			·····					
FACE A	20 20	20 20	12 13	14 15	16 17	18 19	20			
FACE B		<u> </u>	<u></u>							
TRANSFER S18 11 02									TR	ANSFER D3
FACE A	20 20	20 20	12 13	14 15	16 17	18 19	20 11	11 11		
FACE B	11									
TRANSFER S19 98 BO										
FACE A										
FACE B	11 98	B0								
TRANSFER S20 F2 FC										
FACE A										
FACE B	11 98	B0 F2						1		
TRANSFER S21 AB 03										
FACE A					<u> </u>					
FACE B	11 98	B0 F2	AB AB	AB AB	AB					
TRANSFER S22 FF FE				·			· · · · ·			
FACE A				·						
FACE B	11 98	B0 F2	AB AB	AB AB	AB FF	FÉ				
TRANSFER S23 FC FD										
, , , , , , , , , , , , , , , , , , ,										
FACE A										
FACE A	11 98	B0 F2	AB AB	AB AB	AB FF	FE FC	FD			
FACE A	11 98	B0 F2	AB AB	AB AB	AB FF	FE FC	FD		TR	ANSFER D4
FACE A									TR	ANSFER D4
FACE A FACE B TRANSFER S24 FE FF						FE FC		FF FF	TR	ANSFER D4



SETTING CONDITION NO VERTICAL LINE REARRANGEMENT

TOTAL NUMBER OF DEVELOPED BYTES: 64 BYTES(16 × 4)

NUMBER OF BYTES IN 1 LINE: 16BYTES NUMBER OF DEVELOPED LINES: 4 LINES

	LOCAL MEMORY
	D1 — 01 01 01 02 78 55 44 FF FF FF FF FF 11 11 11
F I G. 11A	00 00 00 00 00 00 00
	00 00 00 00 00 00 00
	00 00 00 00 00 00 00 00 00
	00 00 00 00 00 00 00
	00 00 00 00 00 00 00
	01 01 01 02 78 55 44 FF
	FF FF FF FF 11 11 11
F I G. 11B	D2-62 12 77 45 89 10 55 10
	10 10 10 10 10 20 20 20
	$00 \ 00 \ 00 \ 00 \ 00 \ 00$
	00 00 00 00 00 00 00 00 00
	00 00 00 00 00 00 00
	01 01 01 02 78 55 44 FF
	FF FF FF FF 11 11 11
F I G. 11C	62 12 77 45 89 10 55 10
1 1 G. 1 1 G	10 10 10 10 20 20 20
	D3-20 20 20 12 13 14 15
	16 17 18 19 20 11 11 11 00 00 00 00 00 00 00 00
	00 00 00 00 00 00 00

T		4	4	
1	G.			U

	01	01		02			44	FF
	FF	FF	FF	FF	FF	11	11	11
	62	12	77	45	89	10	55	10
	10	10	10	10	10	20	20	20
	20	20	20	20	12	13	14	15
	16	17	18	19	20	11	11	11
D4-	11	98	B0	F2	AB	AB	AB	AB
	AB	FF	FE	FC	FD	FF	FF	FF



F I G. 12

